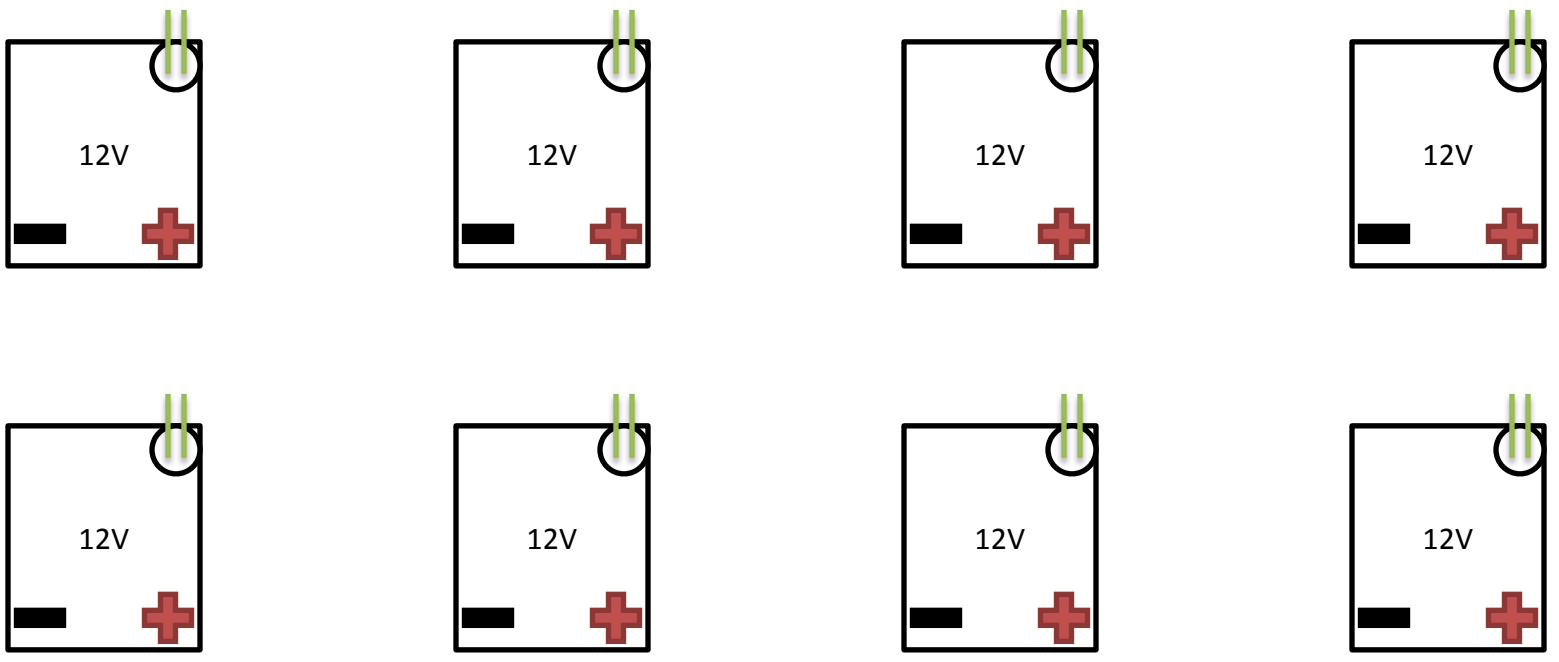
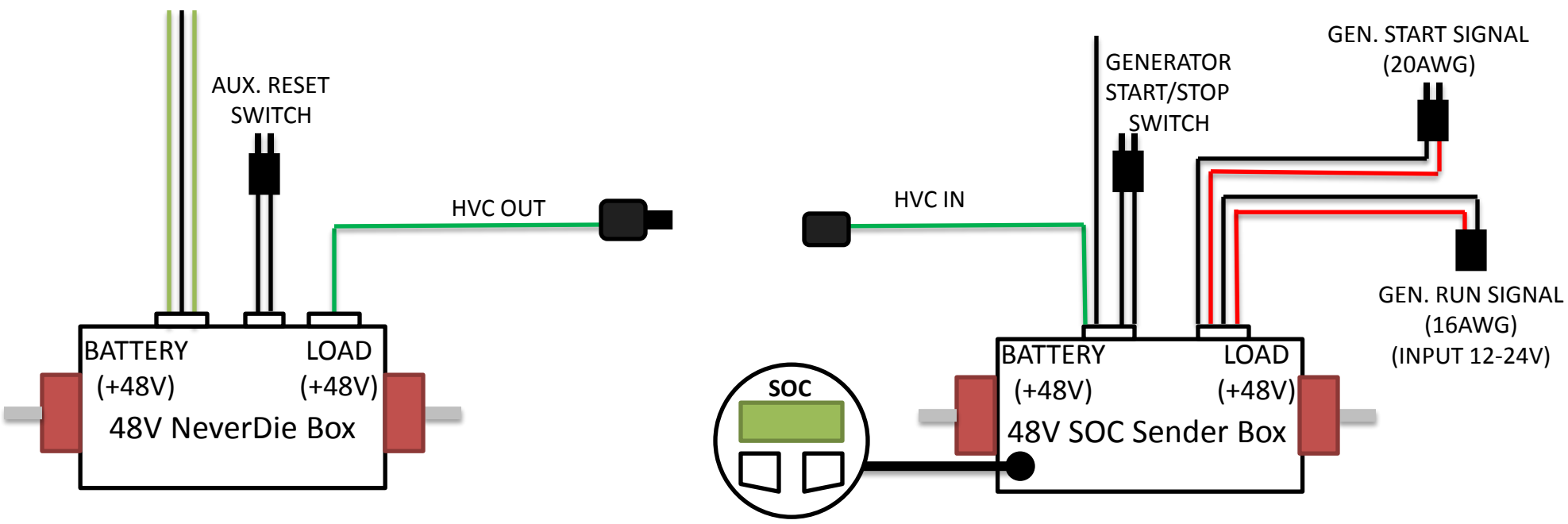
Lithionics Battery

48V Parallel Battery System NeverDie-PSS-SOC
STEP-BY-STEP WIRING DIAGRAM





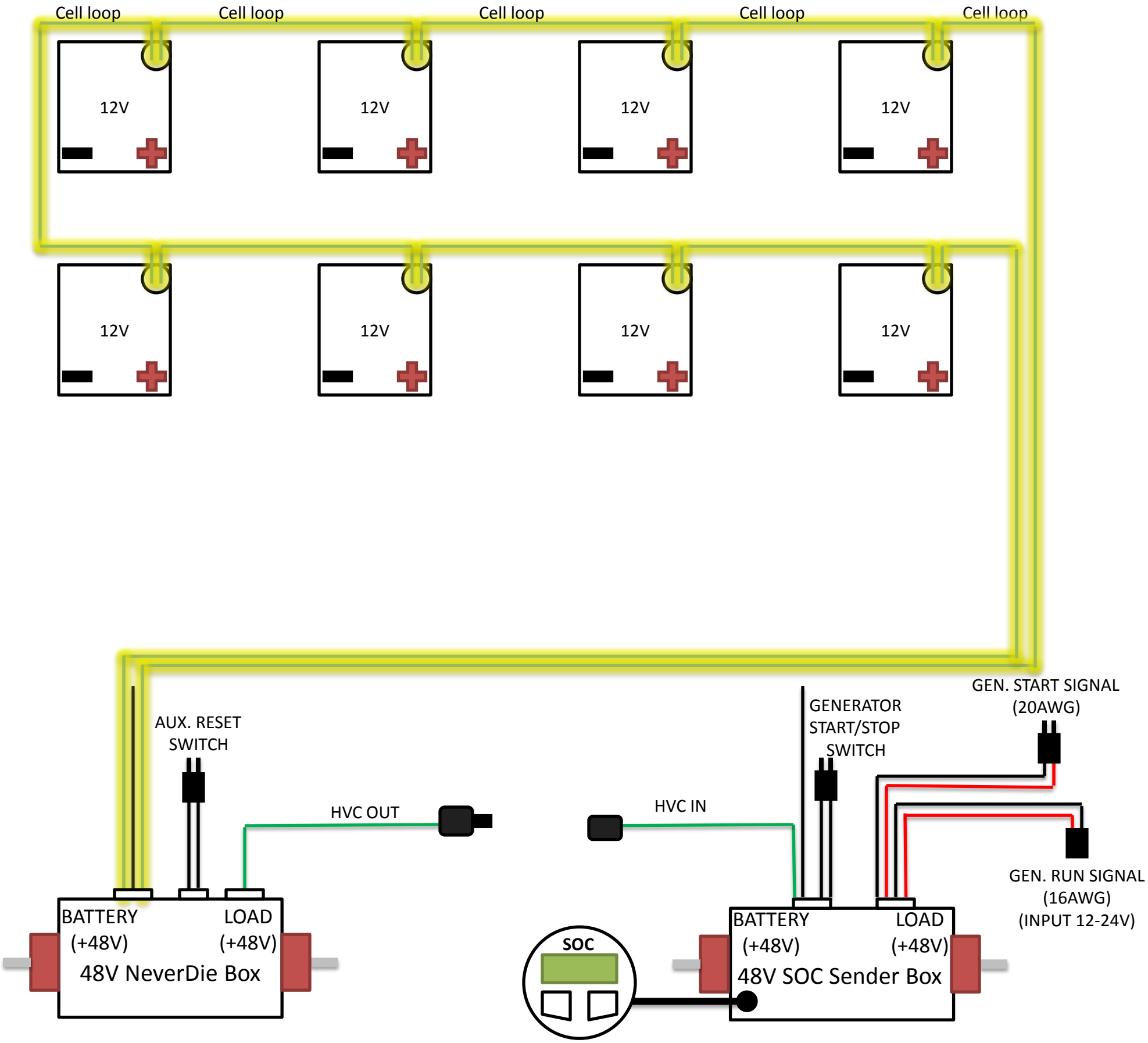
48V
Discharge and Charge
Connection Point

5/2/14
DWG – NM
CKD – CH

ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

Lithionics Battery

STEP 1: Connect the cell loop from each 12V battery module in series to the NeverDie box as shown.



48V
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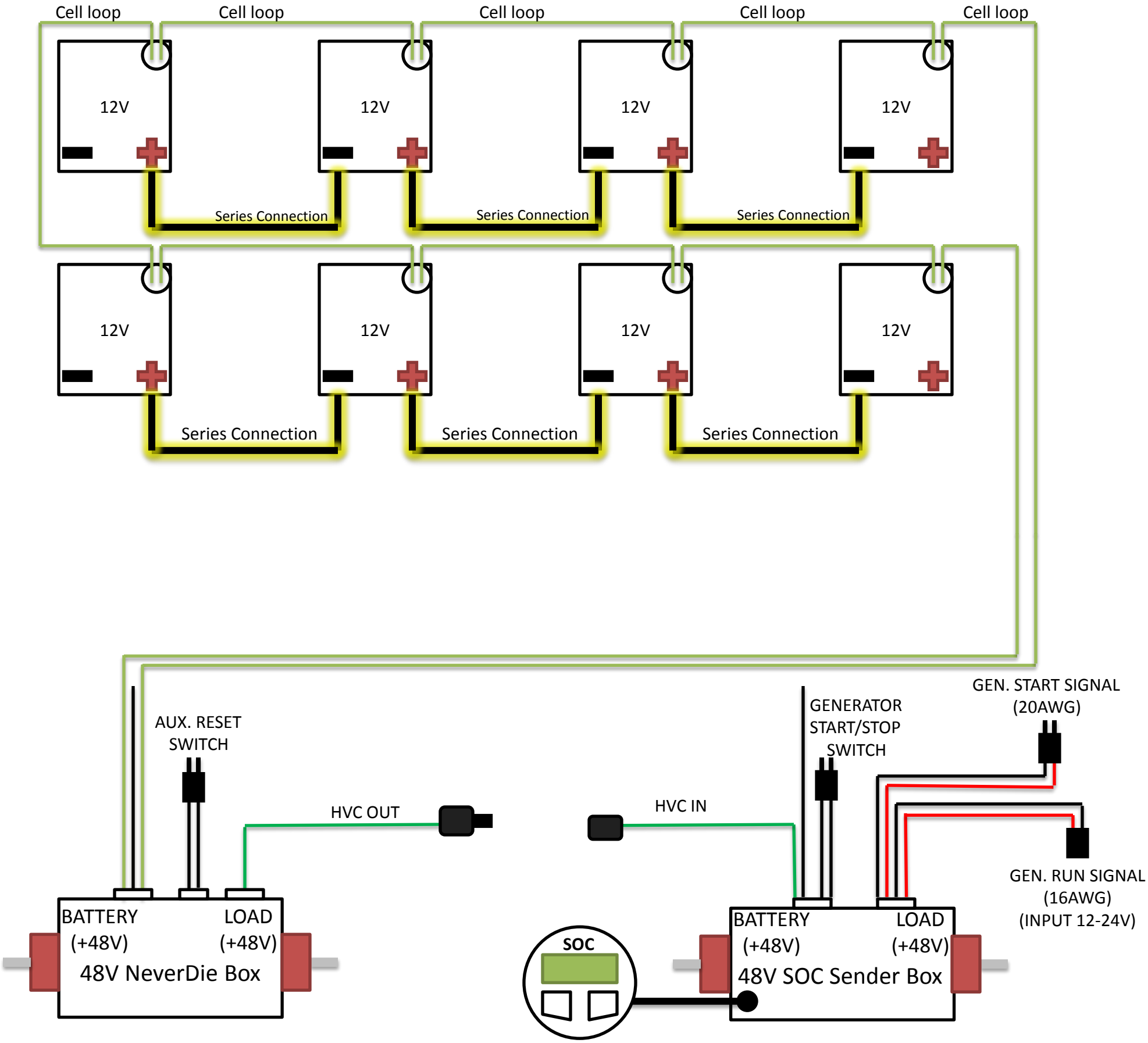
Lithionics Battery

STEP 2: Connect the 12V Battery modules in series to create two 48V battery systems as shown below.



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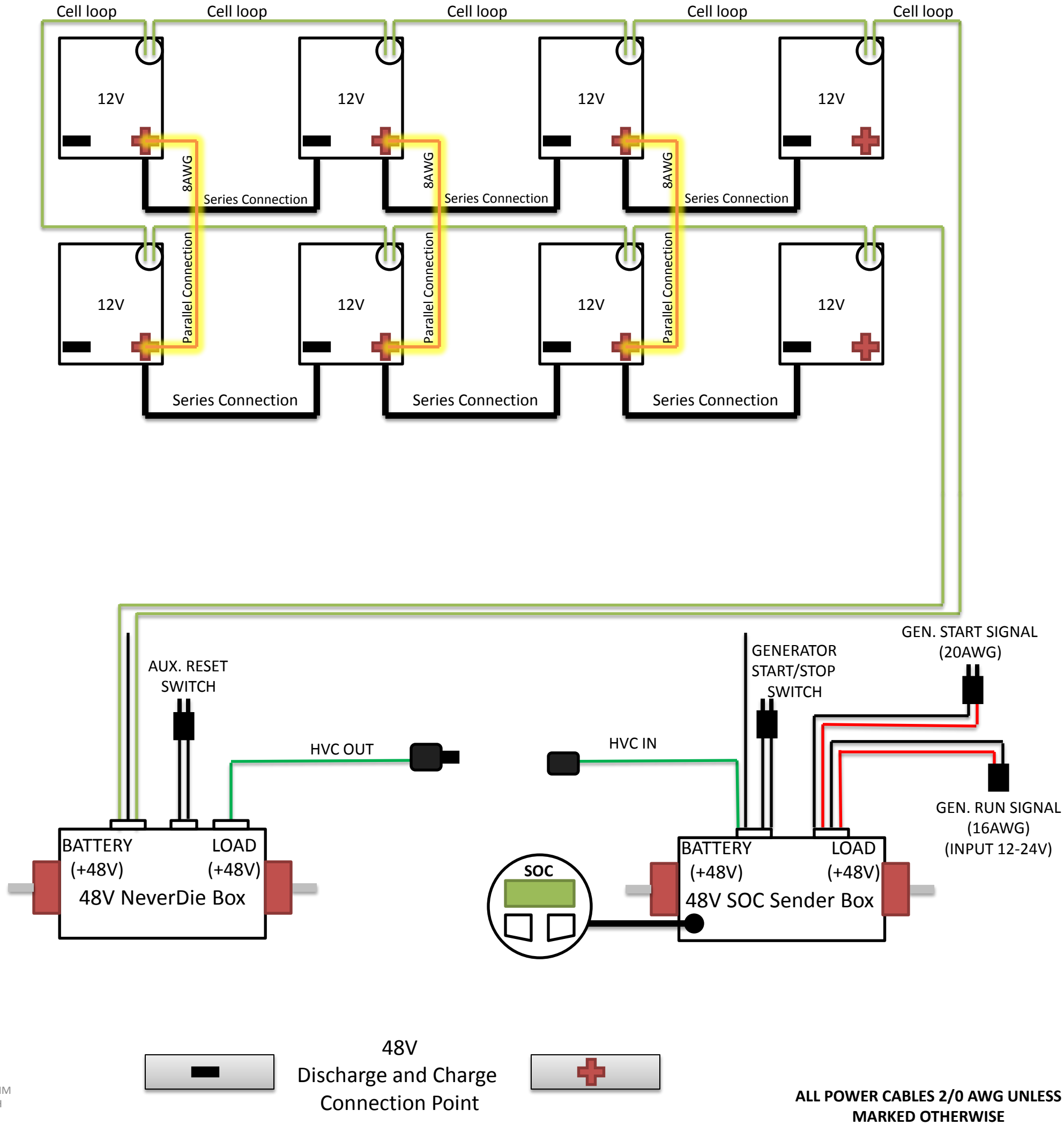
Lithionics Battery

STEP 3: Connect the 12V Battery modules shown in parallel. This is to promote a better balanced system which will increase the performance and life span of the battery system over time.



5/2/14
DWG – NM
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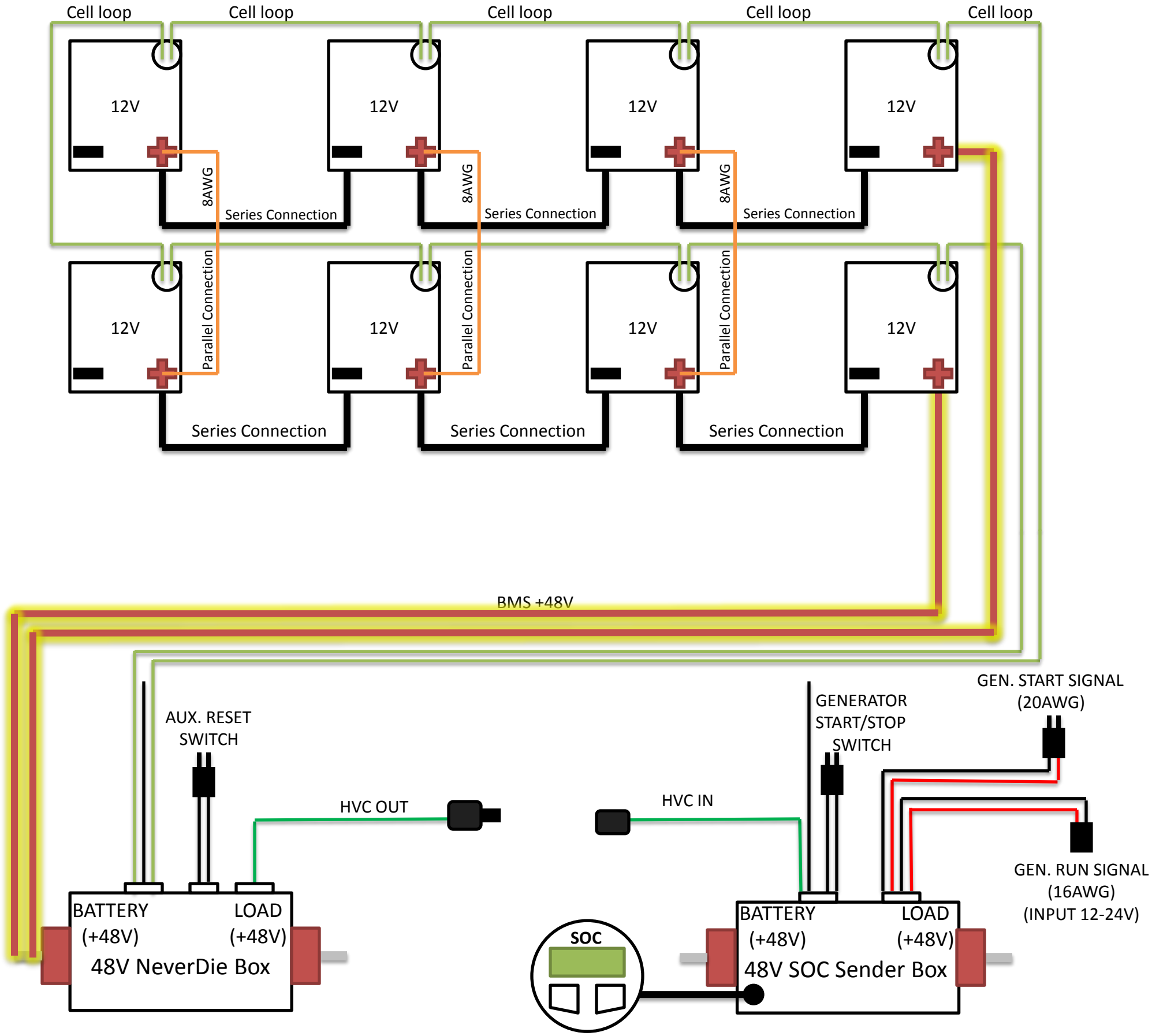
ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

Lithionics Battery

STEP 4: Connect the most positive terminal of each 48V system to the NeverDie box terminal labeled Battery.



48V
Discharge and Charge
Connection Point

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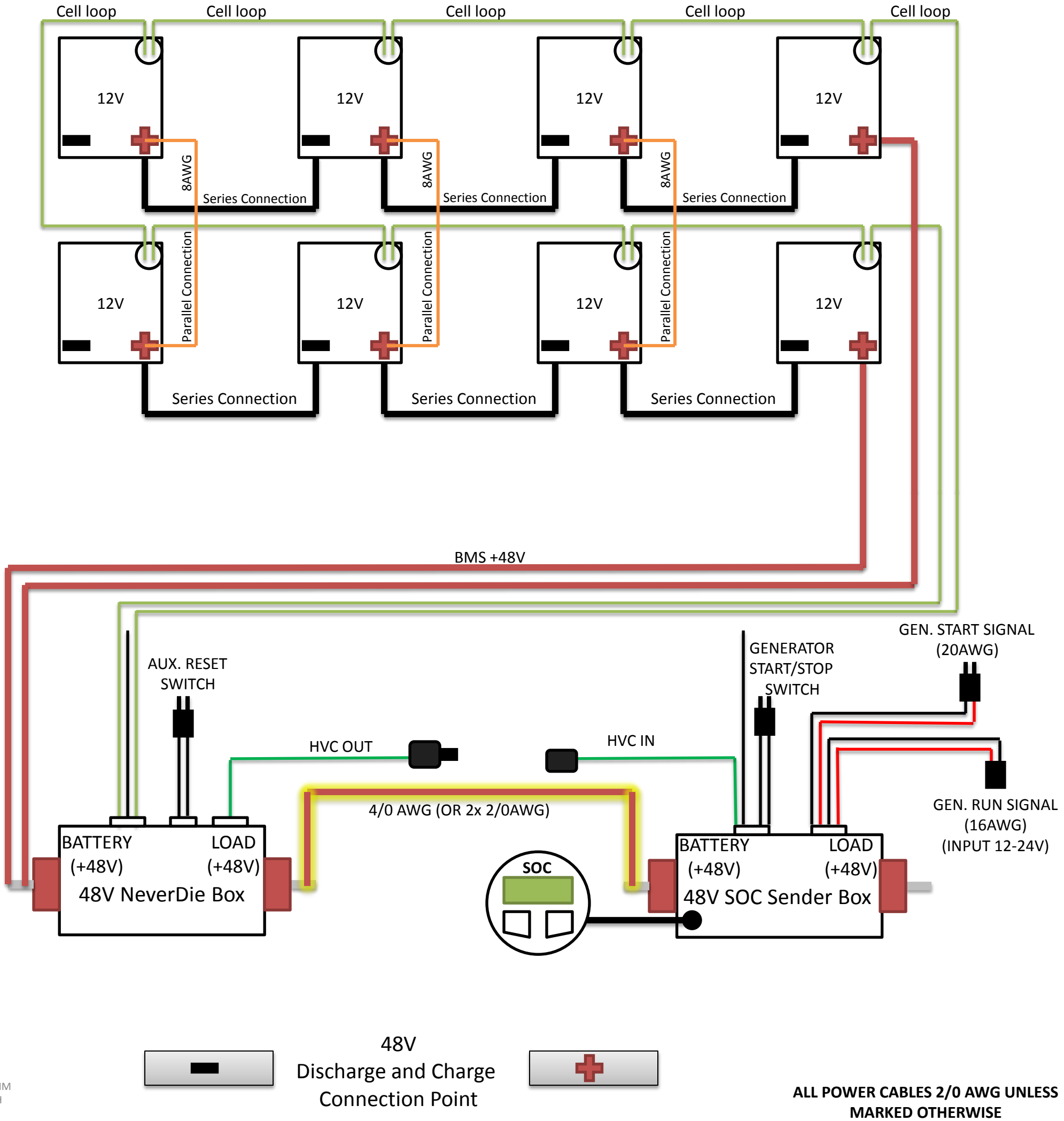
ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

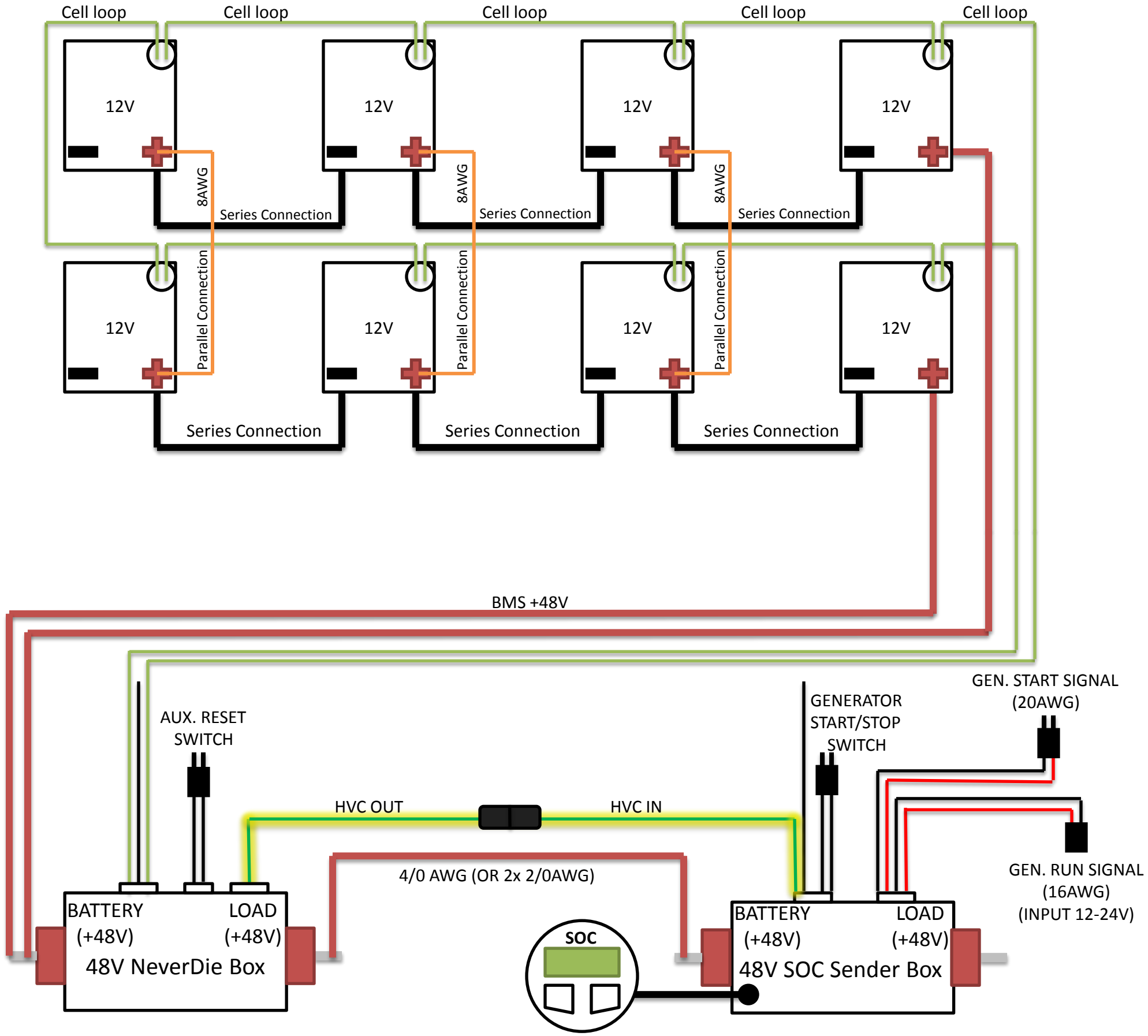
STEP 5: Using a 4/0AWG cable connect the Load terminal of the NeverDie Box to the Battery Terminal of the SOC Sender box.



5/2/14
DWG – NM
CKD – CH

ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

STEP #: Connect the HVC OUT cable from the NeverDie box to the HVC IN cable of the SOC Sender box.

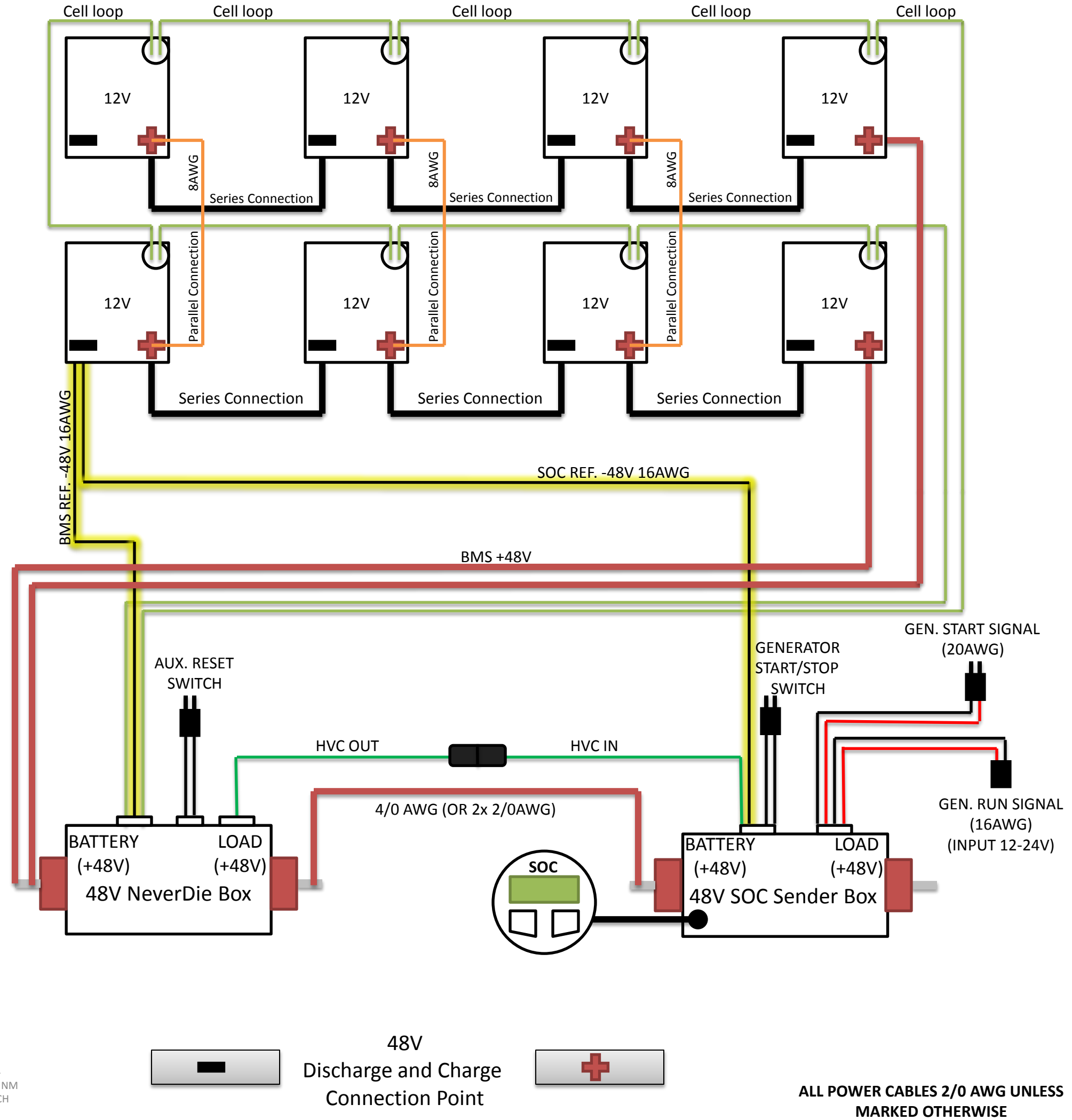


48V Discharge and Charge Connection Point

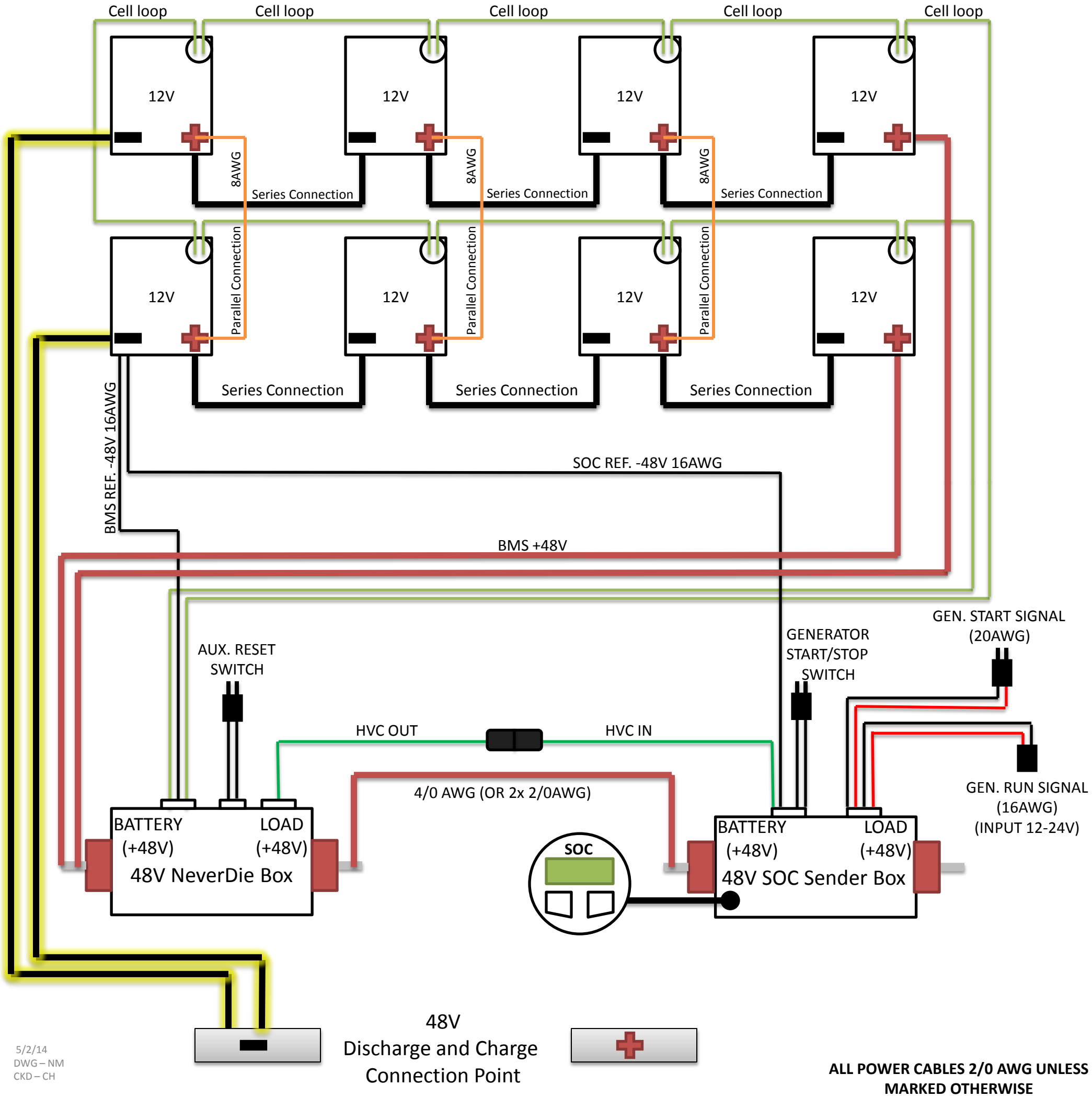
5/2/14
DWG – NM
CKD – CH

ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

STEP 6: Connect the most negative terminal of the closest proximity 48V system to the NeverDie & SOC boxes negative reference wire.



STEP 7: Connect the most negative terminal of each 48V system to the negative discharge & charge bus bar.



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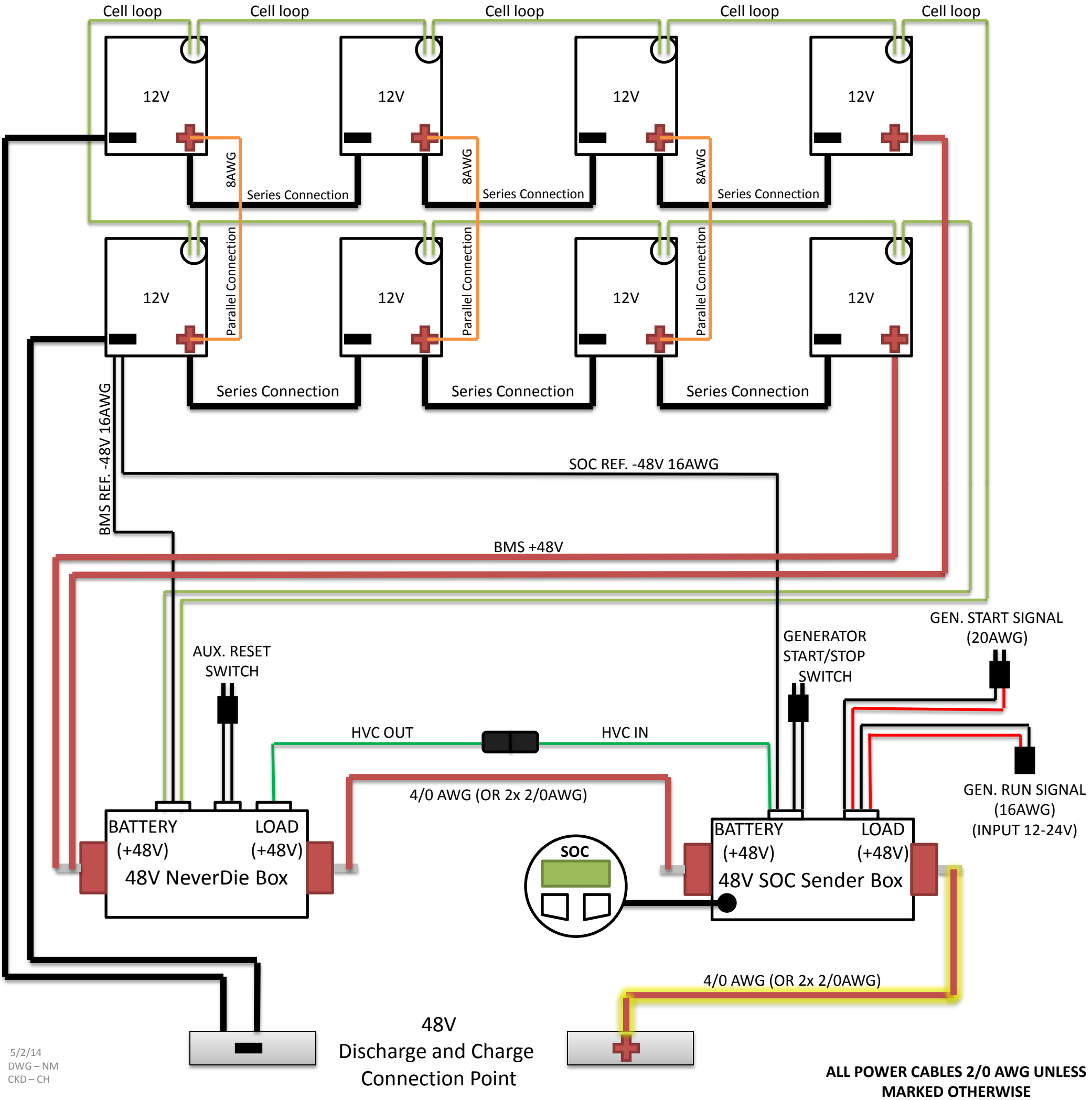
STEP 8: Connect the SOC Terminal labeled LOAD to the +48V discharge & charge bus bar.



5/2/14
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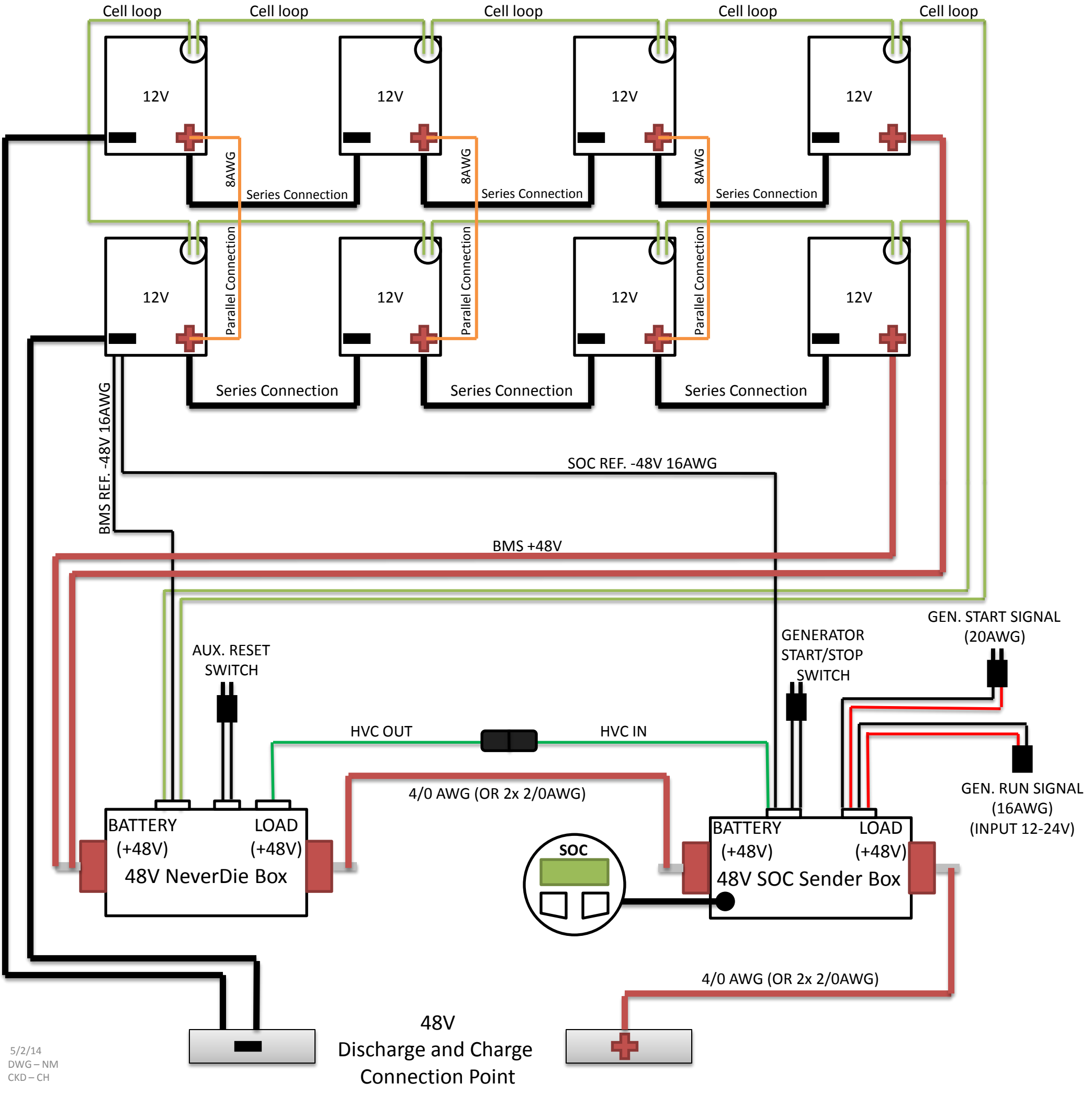
CKD – CH

ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

ALL POWER CABLES 2/0 AWG UNLESS MARKED OTHERWISE

Lithionics Battery

48V Parallel Battery System PSS-SOC



5/2/14
DWG – NM
CKD – CH